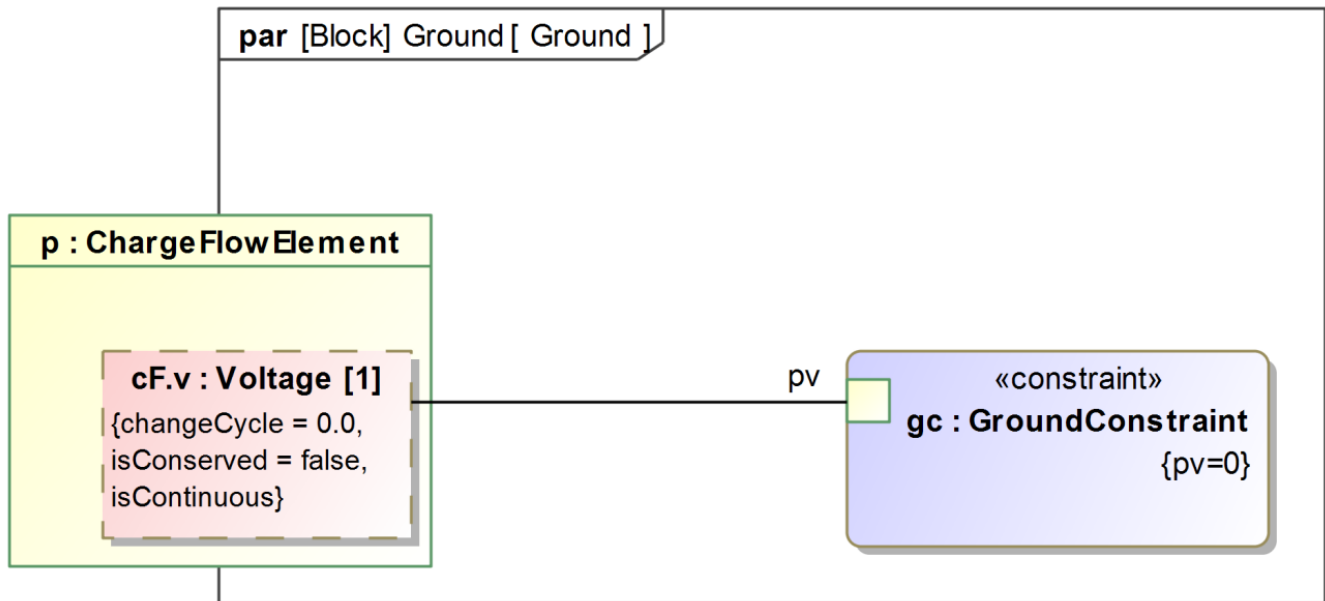


Constraint properties and bindings

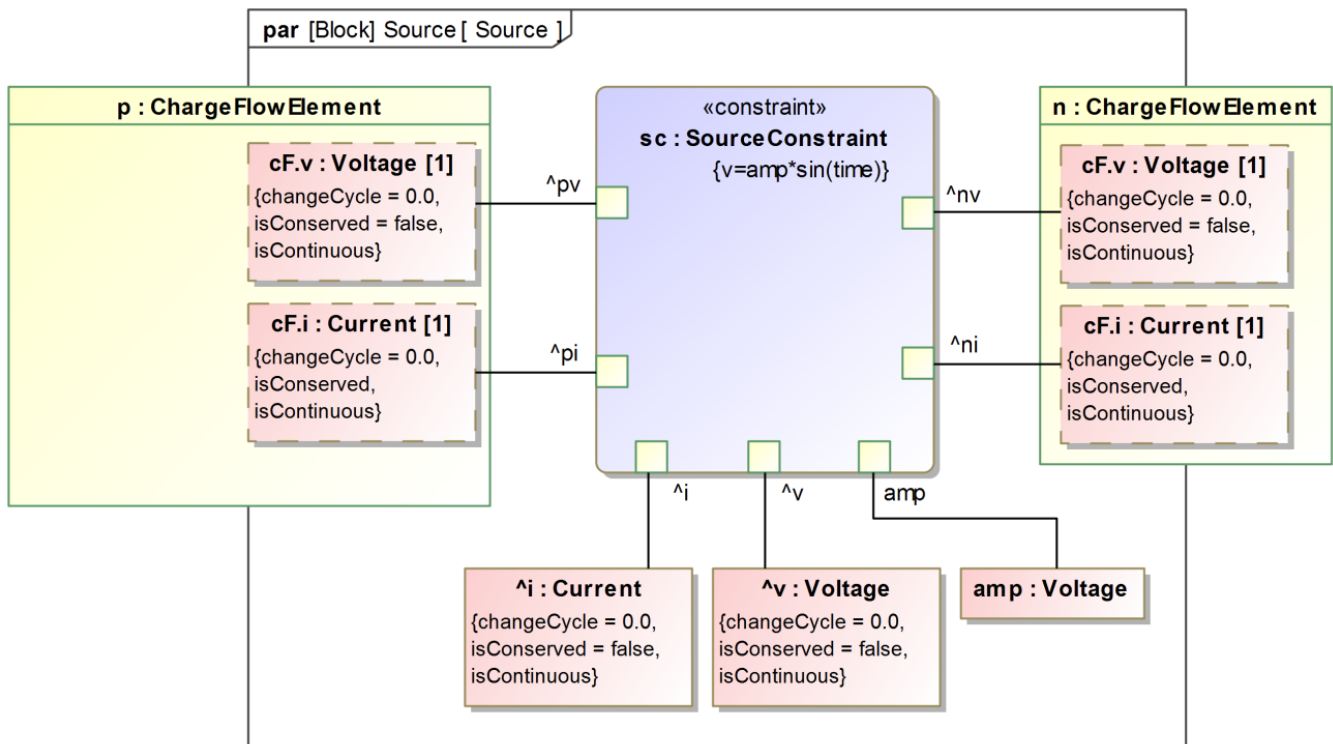
The previously created constraints need to be bound to the variable of each block. To do so, constraint properties are created in each block, and the block variables and parameters are bound to the constraint parameters accordingly. Images bellow show the bindings for the ground, the source, and the resistor respectively.



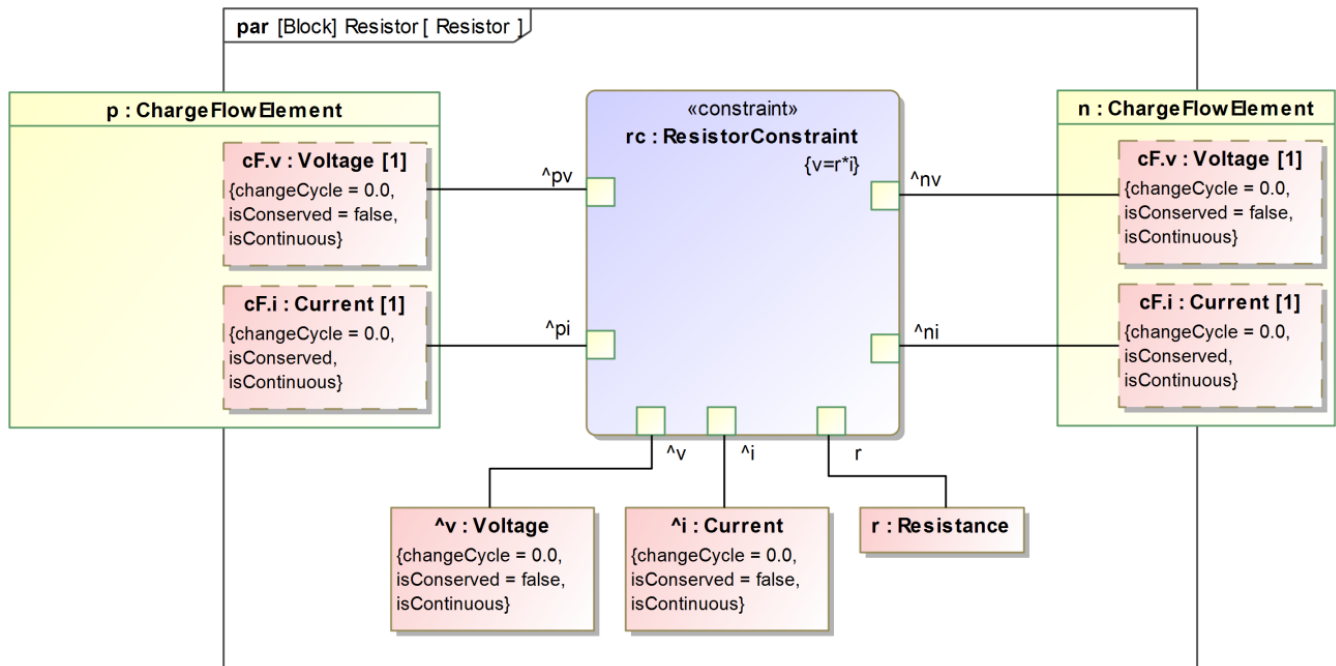
To create bindings, it is necessary to display the ports as well as their parts using the shortcut menu (**Related Elements > Display Ports** and **Related Elements > Display Parts**).



The SysML Parametric Diagram of *Ground* Block.



The SysML Parametric Diagram of *Source* Block.



The SysML Parametric Diagram of *Resistor* Block.